

Energy-Aware System Design: Algorithms And Architectures

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Energy aware software

Energy Aware Software by Energy efficient system design is becoming increasingly It is highly desirable
that we structure our algorithms and systems in such

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View Guilherme Corr a's where I work on video coding algorithms and digital systems design.
Development of algorithms and hardware architectures for

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Law and Architecture and Synthesis for Embedded Systems

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High efficiency video coding (hevc) - algorithms

about the High Efficiency Video Coding (HEVC) include energy-aware signal processing algorithms
and low-power circuit and system design for portable

An architectural co-synthesis algorithm for energy

Abstract. Network-on-Chip (NoC) has been proposed to overcome the complex on-chip communication
problem of System-on-Chip (SoC) design in deep sub-micron.

Photovoltaic energy systems design and

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Current projects - department of computer science

Morphable Computer Architectures for Highly Energy-Aware Systems; PIM: Several projects focusing on performance analysis and scheduling algorithm design.

Dblp: handbook of energy- aware and green

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We focus on the joint design of algorithms, architectures, from energy-aware algorithm development for signal research topics. Energy

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Multi-objective approach for energy- aware

recent works have concentrated on developing energy-aware scheduling algorithms. Transactions on Computer-Aided Design of Integrated Circuits and Systems,

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Design and implementation of energy aware

Design and Implementation of Energy Aware Algorithm important thing that how limited energy resources affect system The main aim is to design an algorithm

Energy- aware scheduling algorithm with

Energy-Aware Scheduling Algorithm With the growing advocacy for green computing system, energy we propose a new energy-aware scheduling algorithm

Citeseerx citation query energy aware task

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Energy- and reliability- aware task scheduling

(2008) ETAHM: an energy-aware task allocation algorithm for multiprocessor systems. In: Design, task scheduling onto heterogeneous MPSoC architectures

Energy- aware software: challenges, opportunities

Architecture agnostic In due course we need to develop algorithms that are energy-aware and that the case for revisiting operating system design for energy

A system-level framework for energy and

A System-Level Framework for Energy and Performance of energy-aware computing systems of NoC architectures and routing algorithms.

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Energy- aware performance analysis methodologies

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Battery modeling for energy aware system design

Battery modeling for energy aware system design (2003) by R Rao, S Vrudhula which provide important inputs required for algorithm and system design (e.g.,

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