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Moldovan, E.; Brehm, G.; Ke Wu; Bosisio, R.G. Radio Frequency Integrated Circuits (RFIC Characteristics of the circuit design Device Modeling

### **Process and device modeling - sciencedirect**

Process and device modeling James D. Plummer Integrated Circuits Laboratory, CIS 114, Stanford University, Stanford, CA 94305, U.S.A. Abstract Computer

### **Three-dimensional process and device modeling**

Three-dimensional process and device modeling S.Selberherr and E. Langer Institute for Microelectronics, Technical University, Gusshausstrasse 27-29,A-1040 Vienna

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Digital BiCMOS Integrated Circuit Design. Authors: Device Modeling. The Springer International Series in Engineering and Computer Science Series Volume 193

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materials in the CMOS process, to implement that circuitry on integrated circuits (chips). CMOS circuitry CMOS VLSI Design: A Circuits and

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Electronic Design Automation for Integrated Circuits Handbook CAT# 3096 Series: design for manufacturability, process simulation, and device modeling.

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T. and Shur, M., Introduction to Device Modeling and Circuit MOS Analog Integrated circuit design Series on integrated circuits and

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device modeling for integrated circuit design : [proceedings of the NATO Advanced Study Institute on Process and device modeling for integrated series\_series

### **Patent us6845347 - method for modeling an**

Method for modeling an integrated circuit including a To design an integrated circuit, simulator with the assistance of a model for the electronic device.

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